

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
Parat, Krishna et al.) Examiner: Not yet assigned
Serial No.: Not yet assigned) Art Unit: Not yet assigned
Filed: Herewith)
For: A NOVEL FLASH INTEGRATED)
CIRCUIT AND ITS METHOD OF)
FABRICATION)
Assistant Commissioner for Patents
and Trademarks
Washington, D.C. 20231

FORMAL DRAWINGS SUBMISSION

Enclosed herewith for submission in the United States Patent and Trademark Office are twelve (12) sheets of formal drawings for the patent application referenced above.

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Respectfully submitted,

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Dated: 5/23, 2001

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(Date signed)

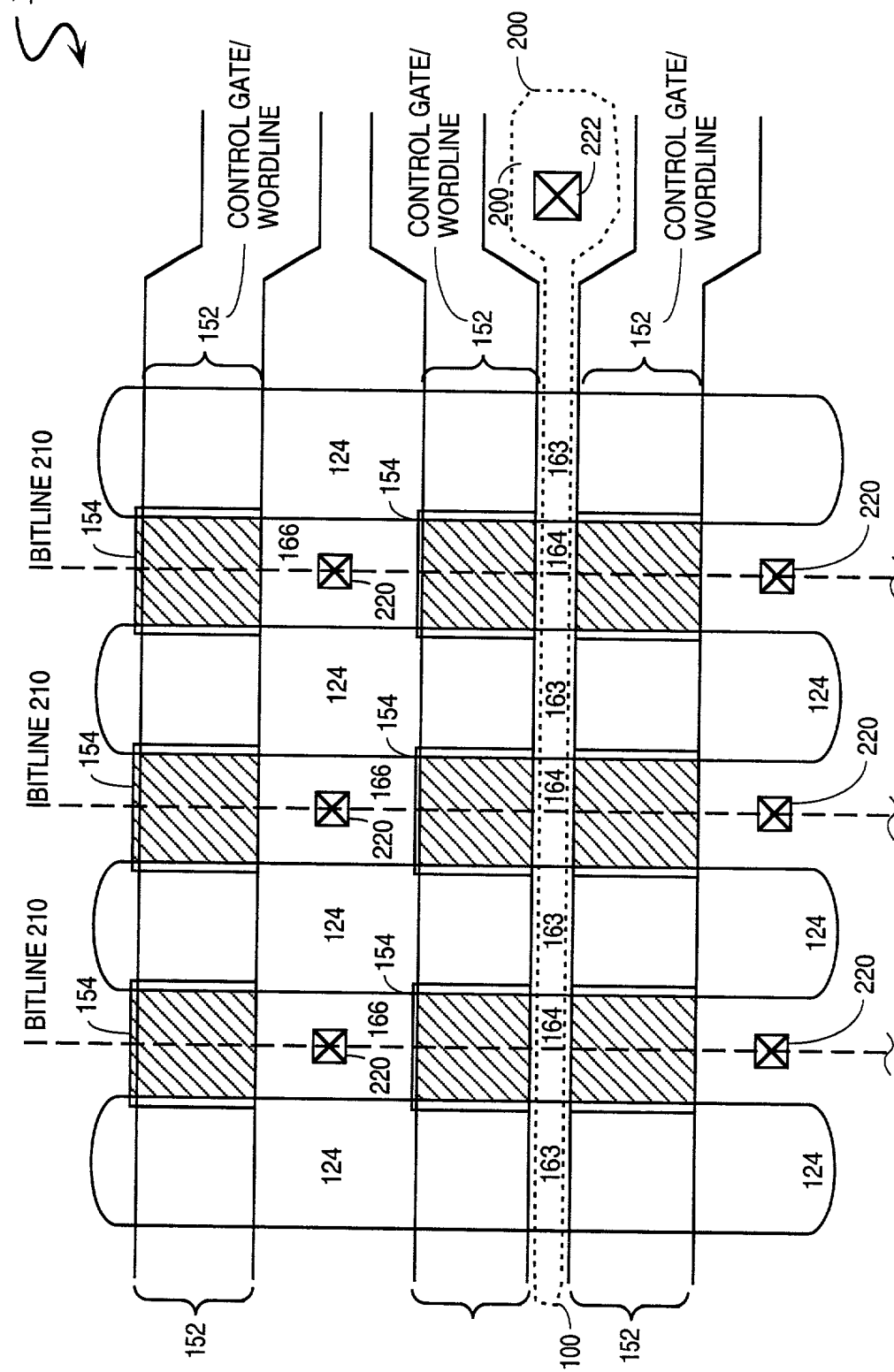


FIG. 1A

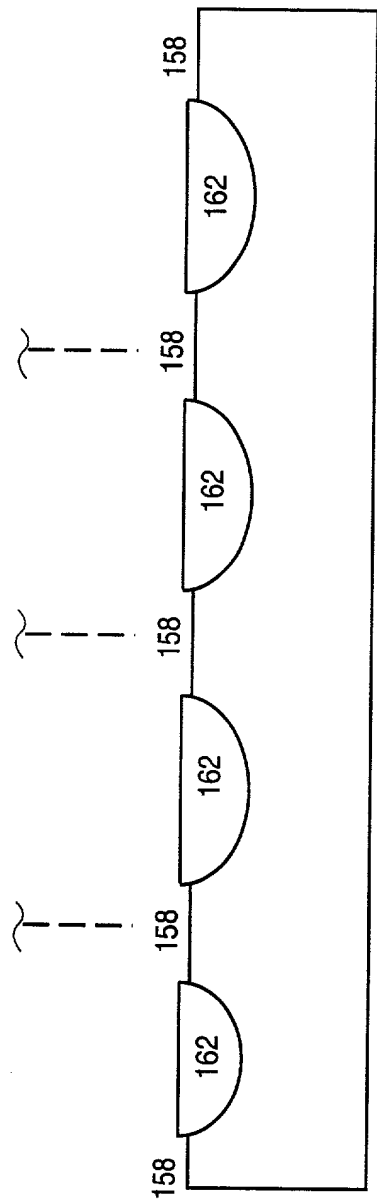


FIG. 1B

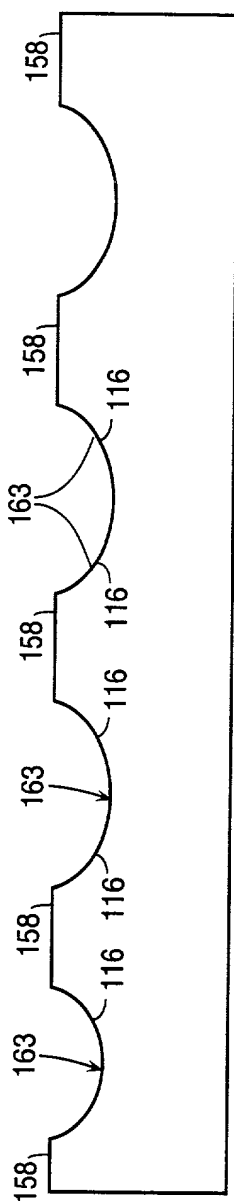


FIG. 1C

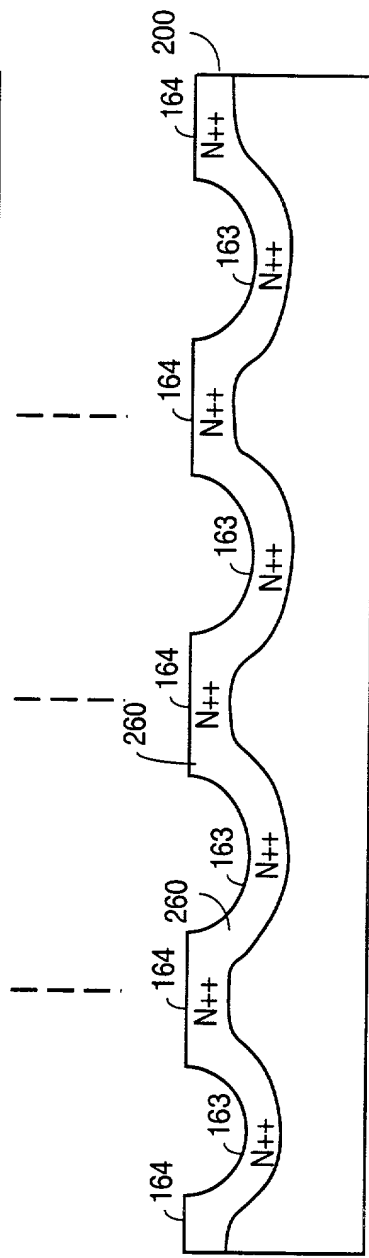


FIG. 1D



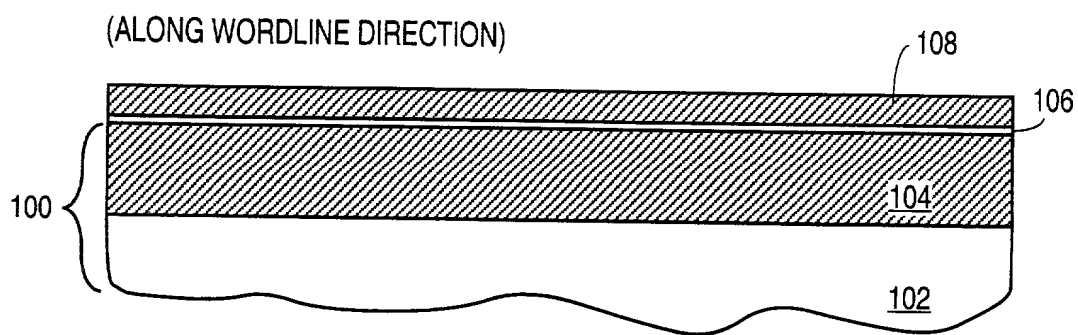


FIG. 2

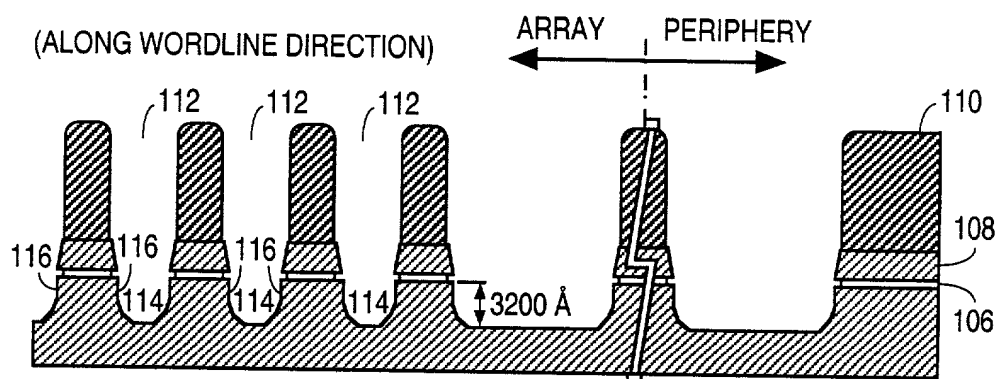


FIG. 3

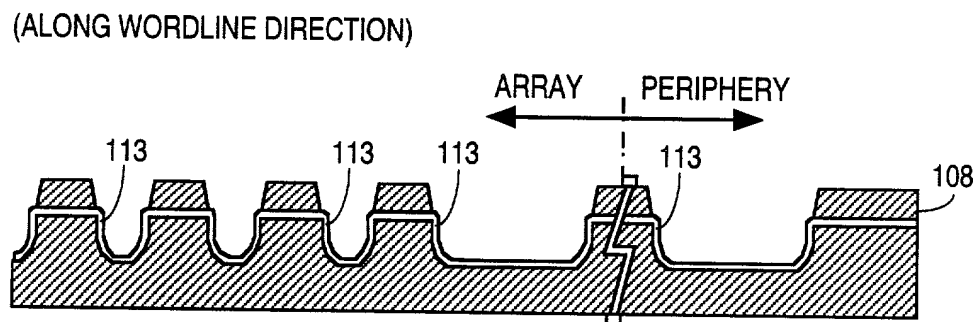


FIG. 4

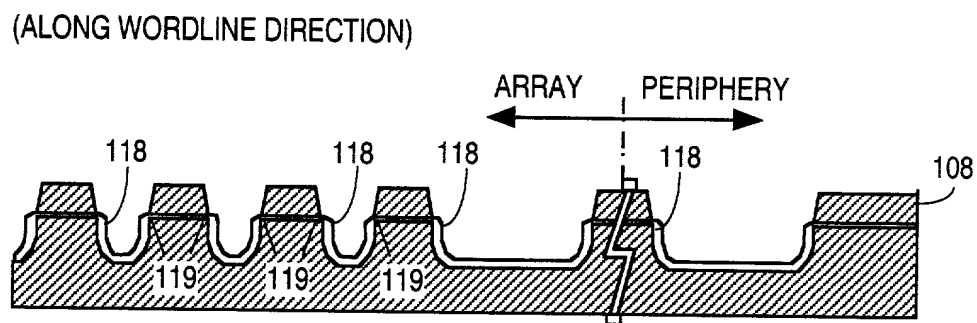


FIG. 5

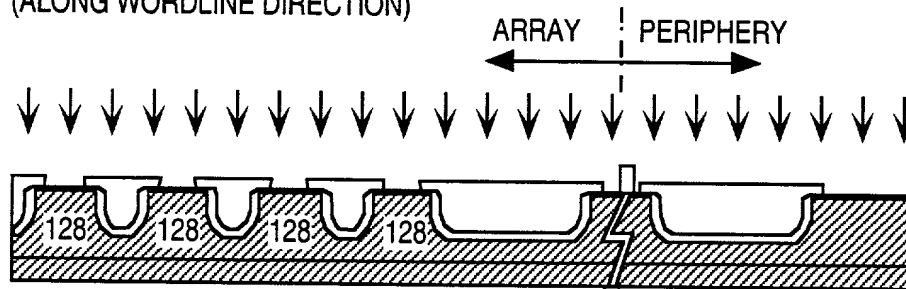
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FIG. 7



FIG. 9



(ALONG WORDLINE DIRECTION)

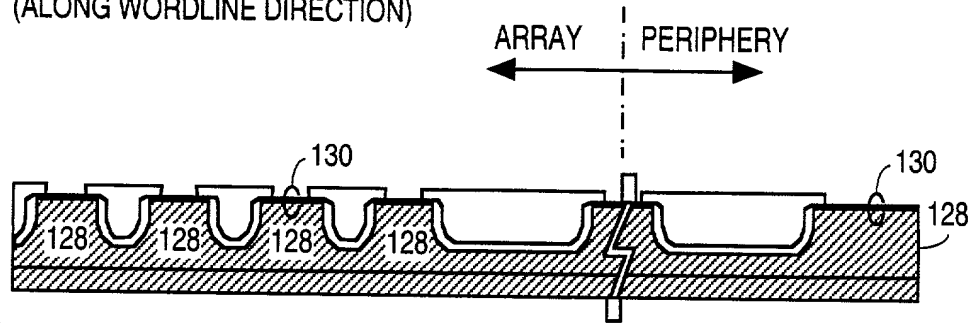


FIG. 10

(ALONG WORDLINE DIRECTION)

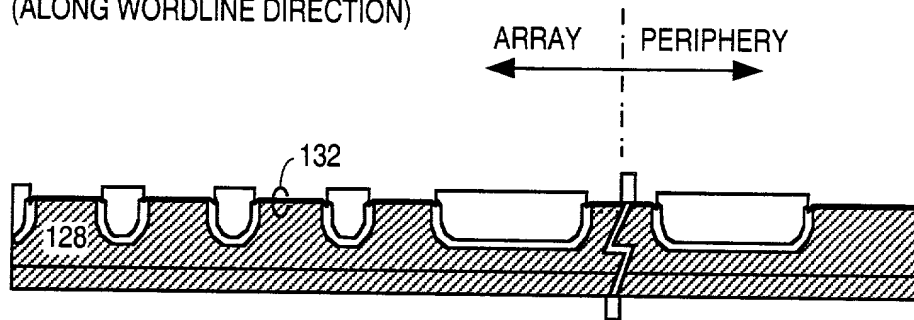


FIG. 11

(ALONG WORDLINE DIRECTION)

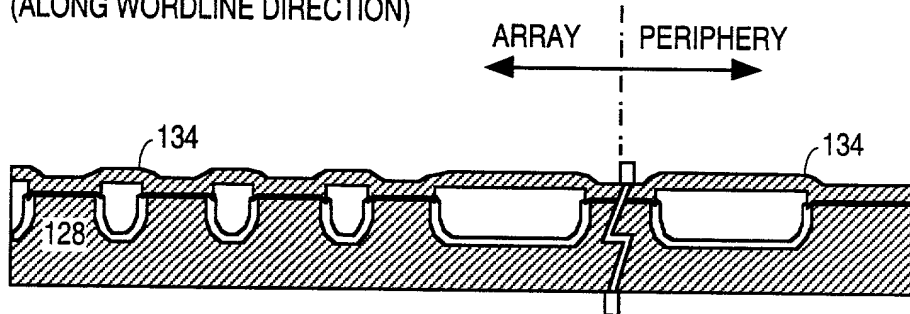


FIG. 12

(ALONG WORDLINE DIRECTION)

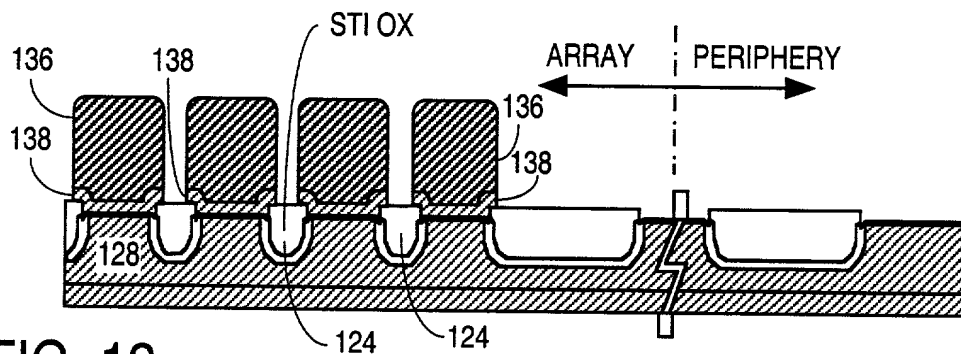


FIG. 13

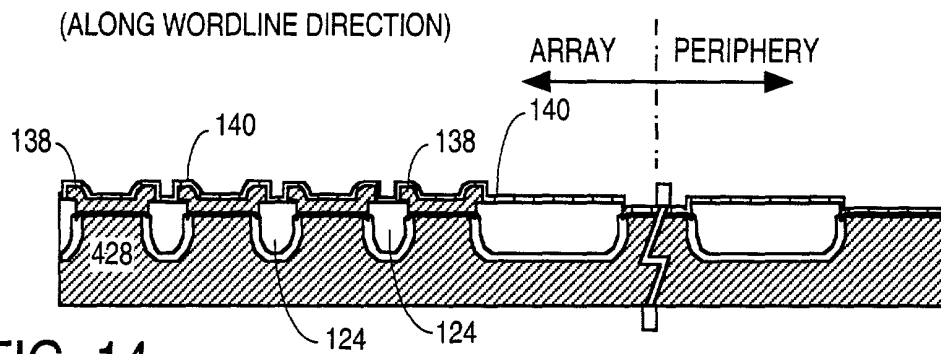


FIG. 14

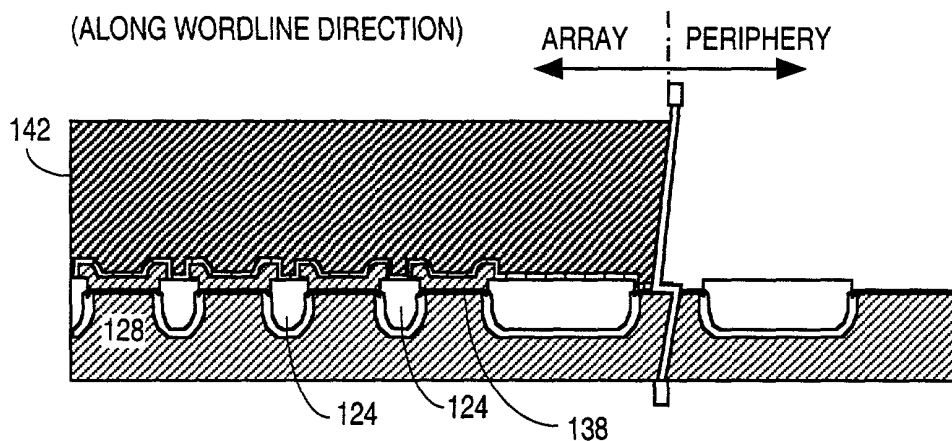


FIG. 15

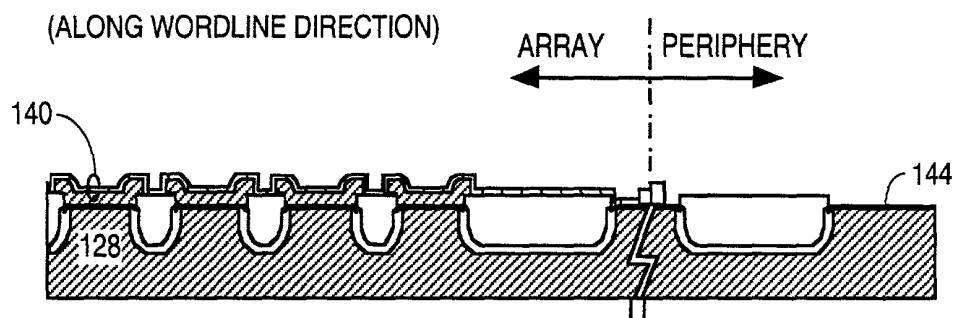


FIG. 16

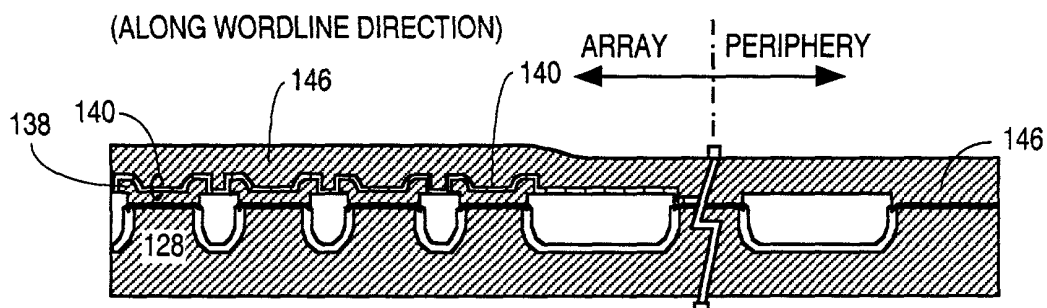


FIG. 17

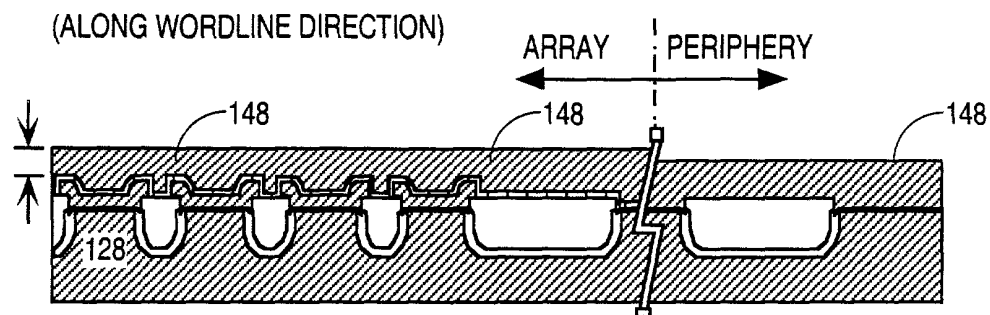


FIG. 18

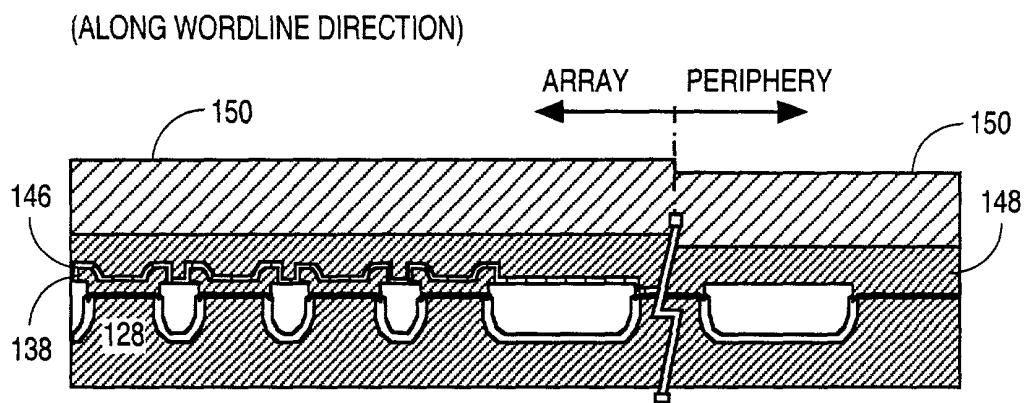


FIG. 19A

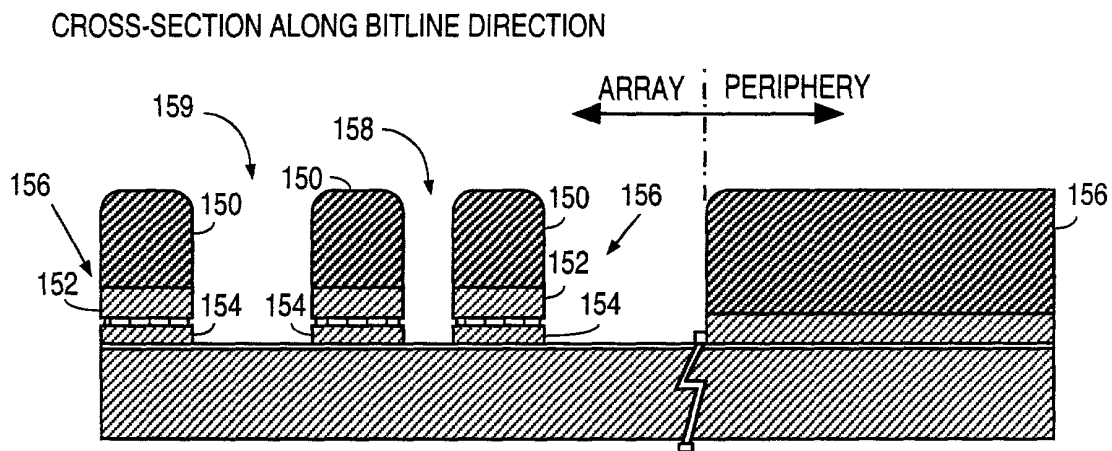


FIG. 19B

THROUGH S/D IN BITLINE DIRECTION

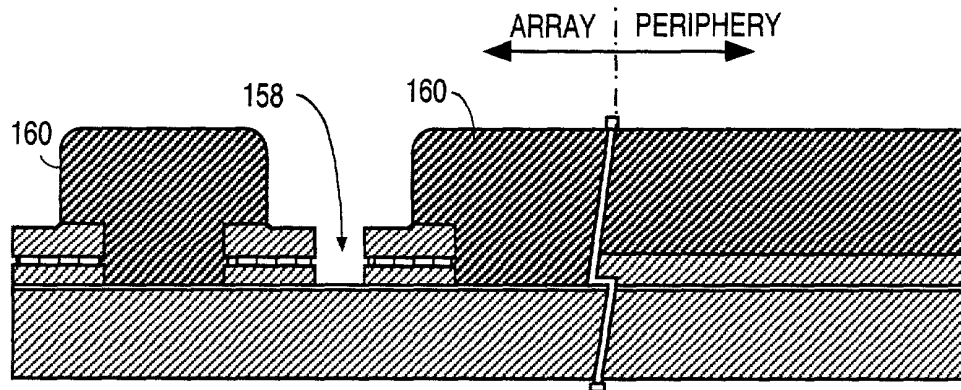


FIG. 20A

THROUGH STI IN BITLINE DIRECTION

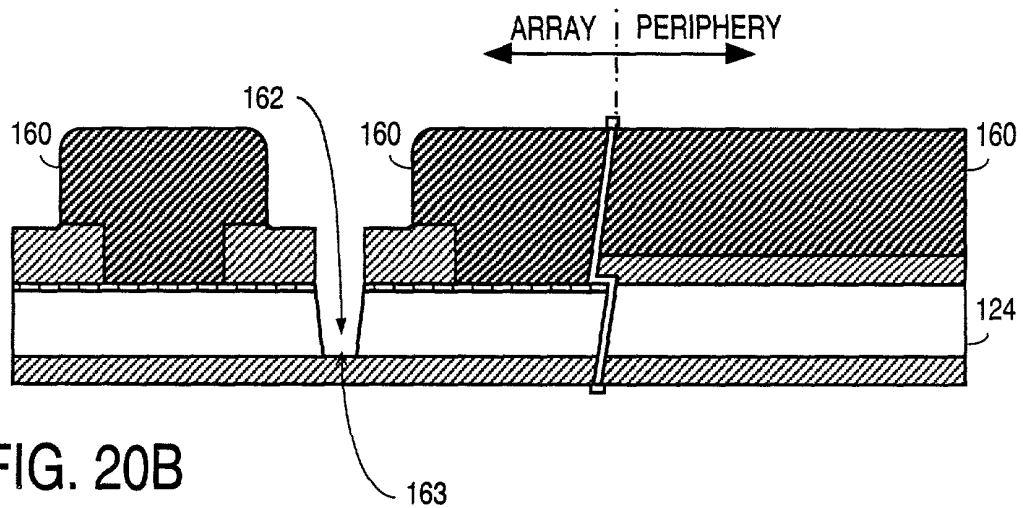


FIG. 20B

ALONG BITLINE DIRECTION

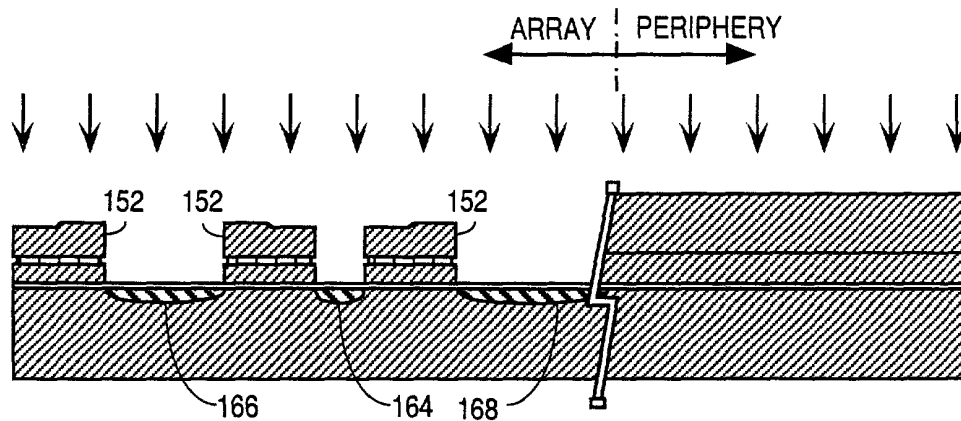


FIG. 21

ALONG BITLINE DIRECTION

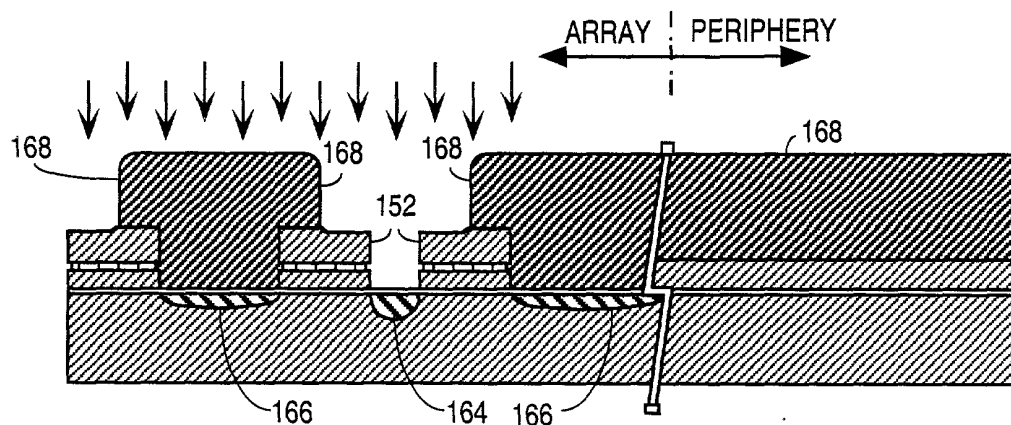


FIG. 22

ALONG BITLINE DIRECTION

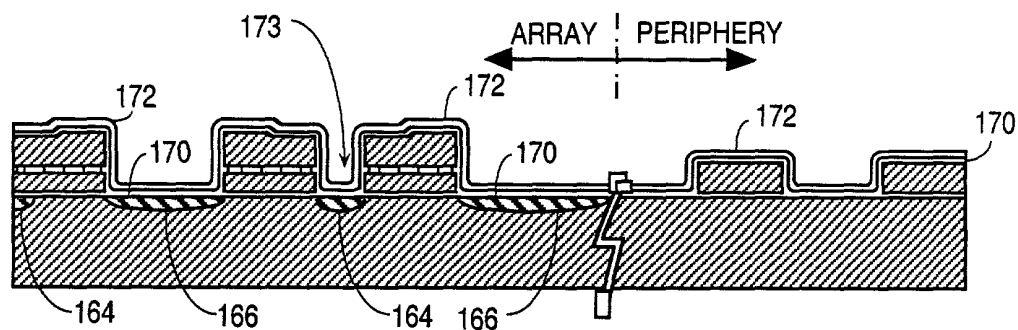


FIG. 23

ALONG BITLINE DIRECTION

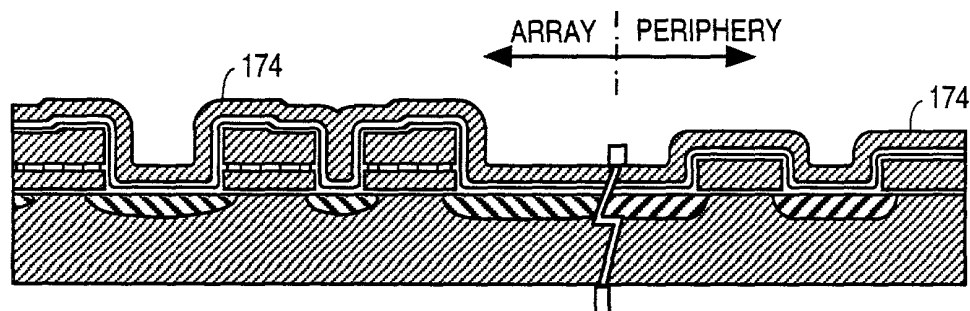


FIG. 24

ALONG BITLINE DIRECTION

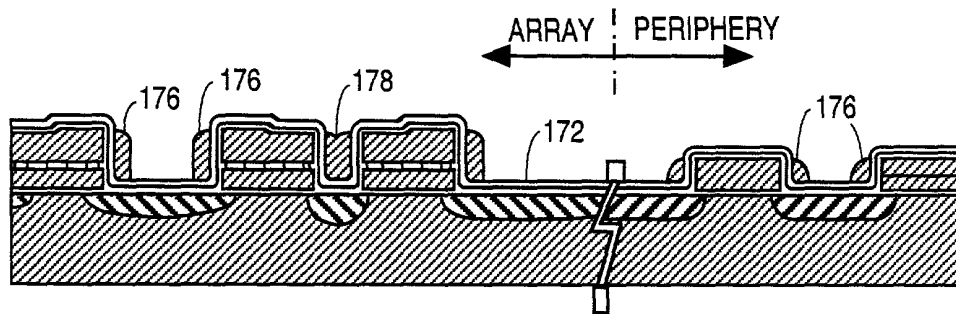


FIG. 25

ALONG BITLINE DIRECTION

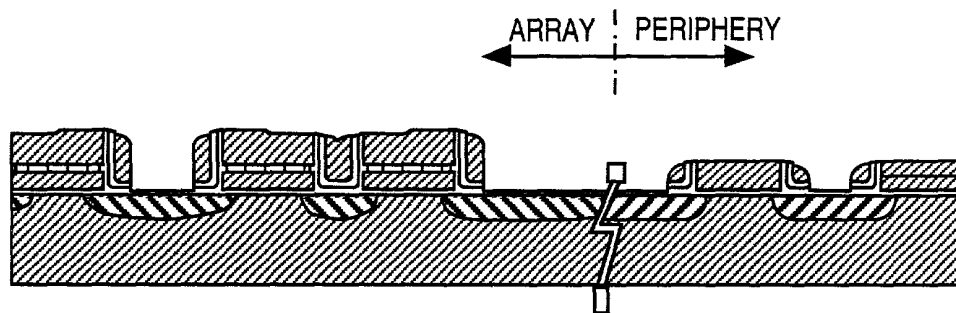


FIG. 26

ALONG BITLINE DIRECTION

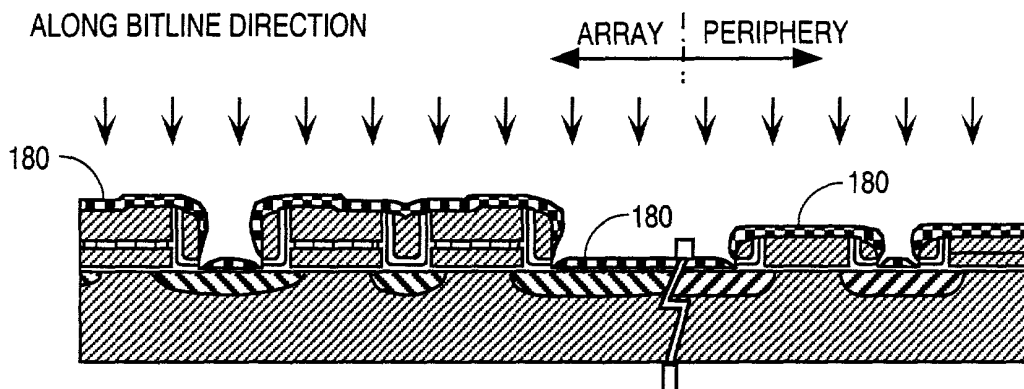


FIG. 27

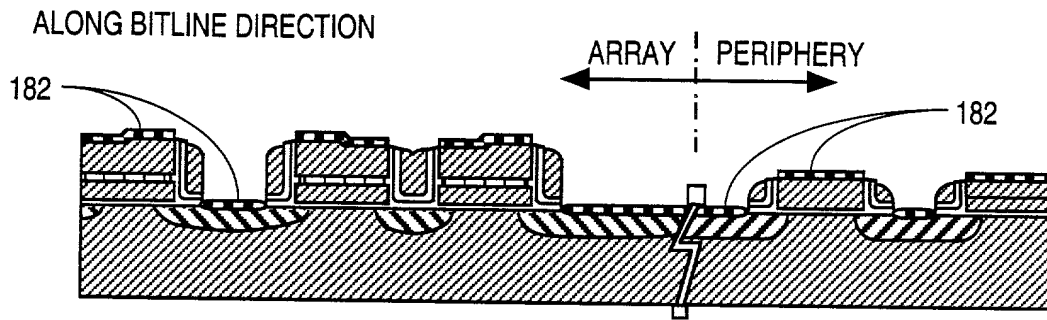


FIG. 28

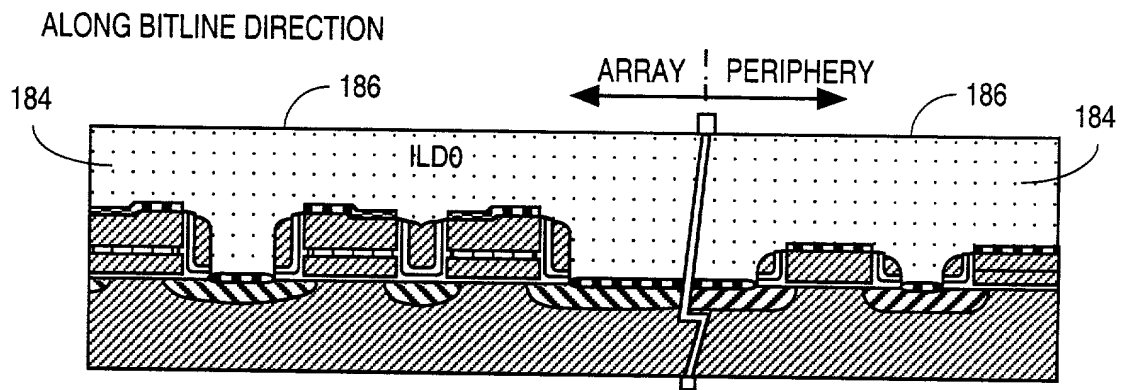


FIG. 29

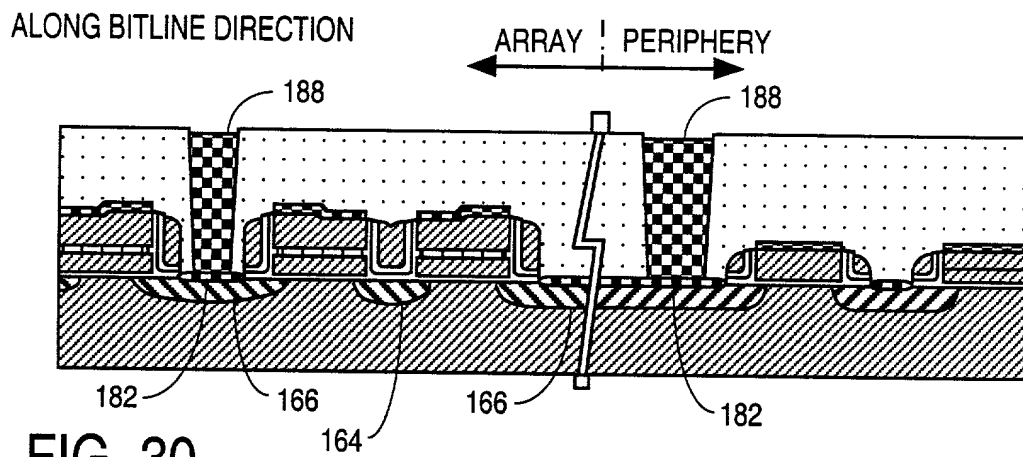


FIG. 30

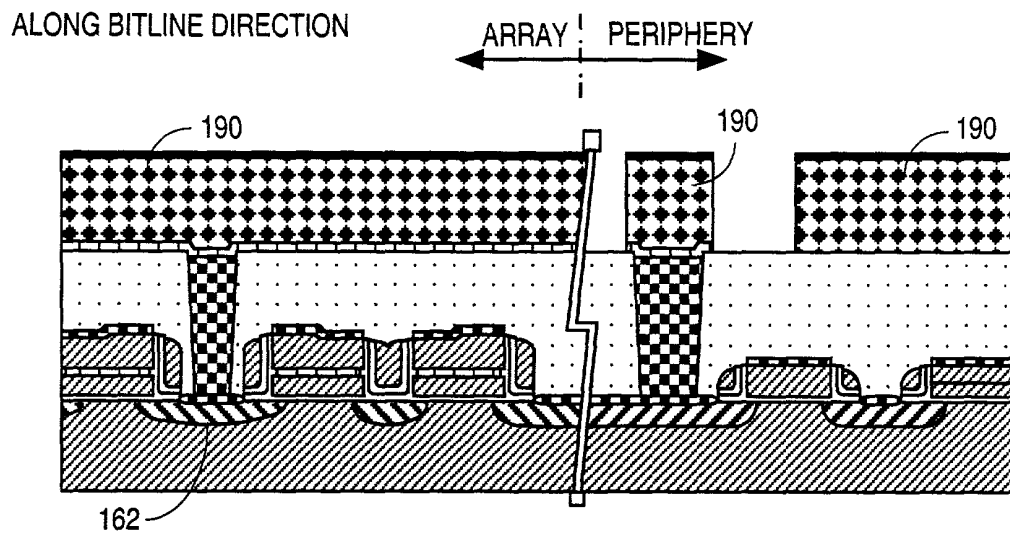


FIG. 31